

Video Solution on Website:-

<https://physicsaholics.com/home/courseDetails/63>

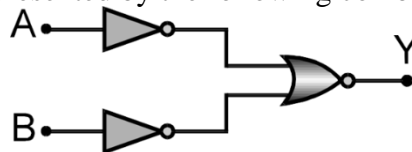
Video Solution on YouTube:-

<https://youtu.be/BxSn5XyyhAc>

Written Solution on Website:-

<https://physicsaholics.com/note/notesDetailis/22>

Q 1. Which logic gate is represented by the following combination of logic gates –

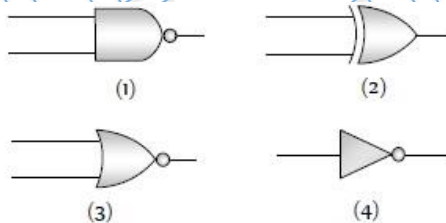


- (a) OR
(b) NAND
(c) AND
(d) NOR

Q 2. Add binary numbers $101 + 110 = ?$

- (a) 1011
(b) 1001
(c) 0111
(d) 111

Q 3. Given below are symbols for some logic gates. The XOR gate and NOR gate respectively are



- (a) 1 and 2
(b) 2 and 3
(c) 3 and 4
(d) 1 and 4

Q 4. The following truth table corresponds to the logic gate

A	0	0	1	1
B	0	1	0	1
X	0	1	1	1

- (a) NAND
(b) OR
(c) AND
(d) XOR

Q 5. What will be the input of A and B for the Boolean expression $\overline{(A + B)} \cdot \overline{(A \cdot B)} = 1$

- (a) 0, 0
(b) 0, 1
(c) 1, 0
(d) 1, 1

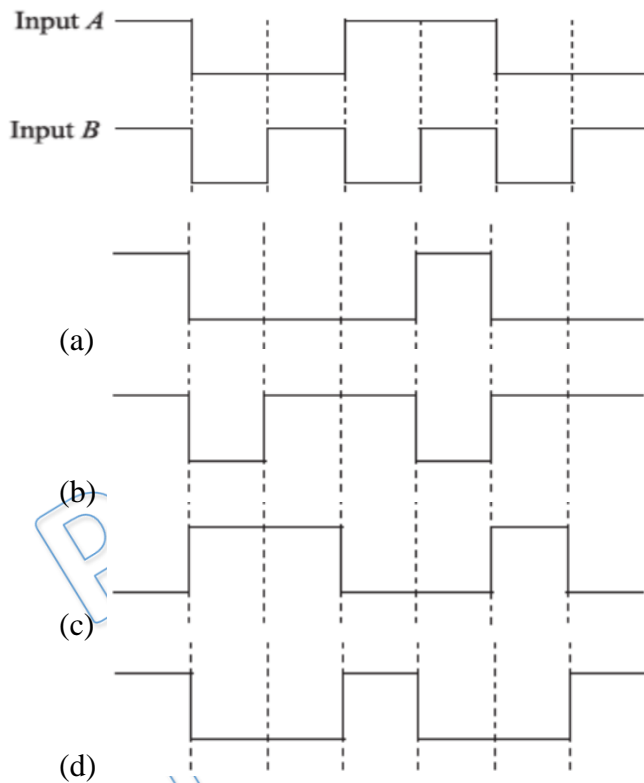
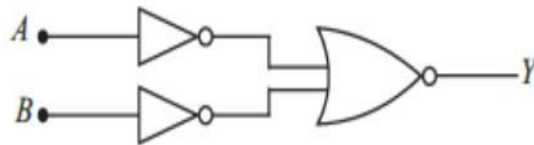
Q 6. Which of the following logic gate is an universal gate

- (a) OR
(b) NOT

- (c) AND (d) NOR

- Q 7. Boolean algebra is essentially based on
 (a) Truth
 (b) Logic
 (c) Symbol
 (d) Numbers

- Q 8. The logic circuit shown below has the input waveforms 'A' and 'B' as shown. Pick out the correct output waveform



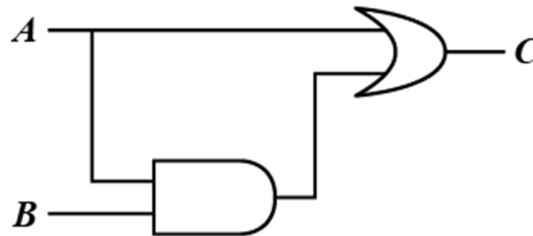
- Q 9. The truth table for NOT gate is

	Input	output		Input	output
	1	1		1	0
(a)	0	0	(b)	0	0
	Input	output		Input	output
	1	0		0	1
(c)	0	1	(d)	1	1

- Q 10. In the Boolean algebra, the following one is wrong

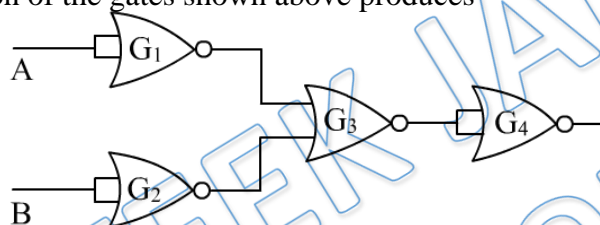
- (a) $1.0 = 0$
- (b) $0.1 = 0$
- (c) $1.1 = 0$
- (d) $1.1 = 1$

Q 11. For the combination of gates shown here, which of the following truth table parts not true?



- (a) $A = 0, B = 1, C = 1$
- (b) $A = 0, B = 0, C = 0$
- (c) $A = 1, B = 1, C = 1$
- (d) $A = 1, B = 0, C = 1$

Q 12. The combination of the gates shown above produces



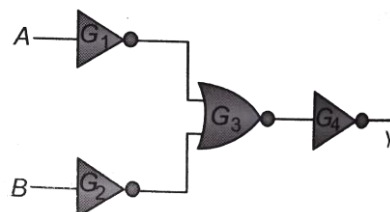
- (a) AND gate
- (b) XOR gate
- (c) NOR gate
- (d) NAND gate

Q 13. The combination of gates shown below yields



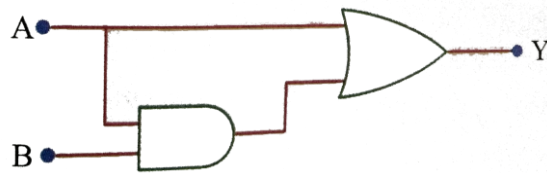
- (a) NAND gate
- (b) OR gate
- (c) NOT gate
- (d) XOR gate

Q 14. The combination of gates shown below produces



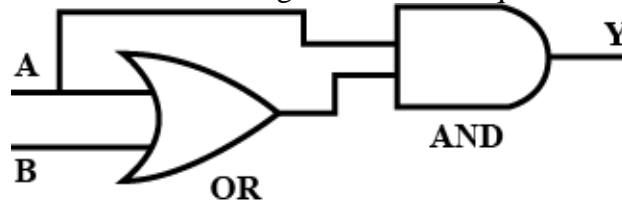
- (a) AND gate
- (b) XOR gate
- (c) NOR gate
- (d) NAND gate

Q 15. The output of the combination of the gates shown in the figure below is



- (a) $A + (A \cdot B)$ (b) $(A+B) A + \bar{B}$
 (c) $(A \cdot B) + (\bar{A} \cdot \bar{B})$ (d) $(A+B) (\bar{A} \cdot \bar{B})$

Q 16. The output Y of the combination of gates shown in equal to:



- (a) A (b) \bar{A}
 (c) $A + B$ (d) $A \cdot B$

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Answer Key

Q.1 c	Q.2 a	Q.3 b	Q.4 b	Q.5 a
Q.6 d	Q.7 b	Q.8 a	Q.9 c	Q.10 c
Q.11 a	Q.12 d	Q.13 b	Q.14 d	Q.15 a
Q.16 a				